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... described. Twenty of these samples (12.5 per cent) tested positive for paraquat. Nine confiscated **hash** oil samples tested negative. The ...
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... k . Examples include block ciphers (eg. DES, in CBC-MAC mode [Ass86]), and key-ed cryptographic **hash** functions (eg. keyed-. MD5 [Tsu92], HMAC [BCK96],etc.) ...
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... word (8 byte) header for each object, which con- tains a pointer to the TIB (type information block located in the immortal space, see below), **hash** bits, lock ...
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Sarang Dharmapurikar, Praveen Krishnamurthy, David E. Taylor

 August 2003 **Proceedings of the 2003 conference on Applications, technologies, architectures, and protocols for computer communications**

 Full text available: [pdf\(207.32 KB\)](#)

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We introduce the first algorithm that we are aware of to employ Bloom filters for Longest Prefix Matching (LPM). The algorithm performs parallel queries on Bloom filters, an efficient data structure for membership queries, in order to determine address prefix membership in sets of prefixes sorted by prefix length. We show that use of this algorithm for Internet Protocol (IP) routing lookups results in a search engine providing better performance and scalability than TCAM-based approaches. The ke ...

Keywords: IP lookup, forwarding, longest prefix matching

2 [Associative mass storage for database](#)

G. Wolf

 March, 1980 **Proceedings of the fifth workshop on Computer architecture for non-numeric processing**

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Two types of associative mass storage for use in database systems, have been introduced in this paper, they are I Hybrid associative storage For use as an associative primary storage, it comprise a storing "basement" and an associative surface matrix. The suggested configuration has a capacity of up to 64 megabytes and a search rate of the order of 2 to 8 gigabytes per second. As well as associative operation, it is also suitable for extremely f ...

3 [A fully associative software-managed cache design](#)

Erik G. Hallnor, Steven K. Reinhardt

 May 2000 **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture**, Volume 28 Issue 2

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As DRAM access latencies approach a thousand instruction-execution times and on-chip caches grow to multiple megabytes, it is not clear that conventional cache structures continue to be appropriate. Two key features—full associativity and software management—have been used successfully in the virtual-memory domain to cope with disk access latencies. Future systems will need to employ similar techniques to deal with DRAM latencies. This paper presents a practical, fully associati ...

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Kenneth L. Calvert, James Griffioen, Su Wen

August 2002 **ACM SIGCOMM Computer Communication Review , Proceedings of the 2002 conference on Applications, technologies, architectures, and protocols for computer communications**, Volume 32 Issue 4

Full text available: pdf(331.84 KB)

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Some end-to-end network services benefit greatly from network support in terms of utility and scalability. However, when such support is provided through service-specific mechanisms, the proliferation of one-off solutions tend to decrease the robustness of the network over time. Programmable routers, on the other hand, offer generic support for a variety of end-to-end services, but face a different set of challenges with respect to performance, scalability, security, and robustness. Ideally, rou ...

Keywords: end-to-end services, ephemeral state, programmable network, router achitecture**2 [Fowarding: Longest prefix matching using bloom filters](#)**

Sarang Dharmapurikar, Praveen Krishnamurthy, David E. Taylor

August 2003 **Proceedings of the 2003 conference on Applications, technologies, architectures, and protocols for computer communications**

Full text available: pdf(207.32 KB)

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Keywords: IP lookup, forwarding, longest prefix matching**3 [Tramp: An interpretive associative processor with deductive capabilities](#)**

William L. Ash, Edgar H. Sibley

January 1968 **Proceedings of the 1968 23rd ACM national conference**

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In recent years; it has become increasingly clear that there is need for a content-addressable computer memory. Larger and larger programs are being written which require a structured data base to operate with any efficiency. Many of these could well benefit by replacing tedious searches with a fast, efficient, "content-addressable" access of the data store. A good example is the "key-word" library search. If one asks for a list of the books written by J. von Neumann ...

4 [Design innovations for embedded processors: A fast on-chip profiler memory](#)

Roman Lysecky, Susan Cotterell, Frank Vahid

June 2002 **Proceedings of the 39th conference on Design automation**

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Profiling an application executing on a microprocessor is part of the solution to numerous software and hardware optimization and design automation problems. Most current profiling techniques suffer from runtime overhead, inaccuracy, or slowness, and the traditional non-intrusive method of using a logic analyzer doesn't work for today's system-on-a-chip having embedded cores. We introduce a novel on-chip memory architecture that overcomes these limitations. The architecture, which we call ProMem ...

Keywords: adaptive architectures, binary tree, embedded CAD, embedded systems, low power, memory design, platform tuning, profiling, system-on-a-chip

5 Associative mass storage for database

G. Wolf

March 1980 **Proceedings of the fifth workshop on Computer architecture for non-numeric processing**

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Jun Xu, Mukesh Singhal

November 1998 **Proceedings of the 5th ACM conference on Computer and communications security**


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August 1999 **ACM Transactions on Information and System Security (TISSEC)**, Volume 2 Issue 3

Full text available:  pdf(143.19 KB)

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A router-based packet-filtering firewall is an effective way of protecting an enterprise network from unauthorized access. However, it will not work efficiently in an ATM network because it requires the termination of end-to-end ATM connections at a packet-filtering router, which incurs huge overhead of SAR (Segmentation and Reassembly). Very few approaches to this problem have been proposed in the literature, and none is completely satisfactory. In this paper we present the hardware design ...

Keywords: TCP/IP, asynchronous transfer mode, firewall, packet filtering, switch architecture

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P Devanbu, SG Stubblebine - Cited by 16 - [Web Search](#)... been strewn with various electronic tamper-**detection** devices ... protection for the entire RAM **content** using as ... We assume the probability of a **hash collision** can be ...Proceedings of IEEE Symposium on Security and Privacy, ..., 1998 - [doi.ieeecomputersociety.org](#) - [doi.ieeeecs.org](#) - [ieeexplore.ieee.org](#) - [seclab.cs.ucdavis.edu](#) - [all 10 versions](#) »

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... Tamper **detection** is usually accomplished by applying some cryp ... to the document and storing this **hash** in a ... **Content** that is accessible more than other con- tent ...

ACM Transactions on Internet Technology, 2001 - [portal.acm.org](#) - [portal.acm.org](#) - [webster.fhs-hagenberg.ac.at](#)

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... stego image SI and resulting payload Y. h 1 ; h 2 : **collision** resistant **hash** ... **hash** value of 160 bit eg SHA-1 or RIPEMD-160 ... 3 Embedding of **detection** watermark: H ...

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... the so called "birthday paradox" that estimates the **collision** probability for ... II.C) that is based on the secure **hash** function H ... replay **detection** or message ...

Computer Networks, 1999 - [eurecom.fr](#) - [cs.plu.edu](#) - [portal.acm.org](#)

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... abstracting [Damashek 1995; Cohen 1995], spelling error **detection** and correction ...

The literature of **hash** functions and **collision** resolution schemes is ...

ACM Transactions on Information Systems, 1997 - [portal.acm.org](#) - [portal.acm.org](#)

[PDF] A low-bandwidth network file system

A Muthitacharoen, B Chen, D Mazieres - [Cited by 76](#) - [Web Search](#)

... RPC li- braries hold messages in **memory** to unmarshal ... the recomputed SHA-1 value to detect **hash** collisions in ... a low but non-negligible probability of **collision**. ...

Proceedings of the 18th ACM Symposium on Operating Systems ..., 2001 - [portal.acm.org](#) - [cs.cornell.edu](#) - [amsterdam.lcs.mit.edu](#) - [scs.cs.nyu.edu](#) - [all 38 versions »](#)

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MD Cooper... - [Cited by 5](#) - [Web Search](#)

... Despite the changing nature of the **content** of informa- ... inability to replicate results because **database** or system. ... tional disk space, main **memory**, and/or central ...

Journal of the American Society for Information Science, 1998 - [doi.wiley.com](#) - [beachmat.berkeley.edu](#) - [beachmat.sims.berkeley.edu](#) - [portal.acm.org](#) - [all 6 versions »](#)

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... 1 These resources include compute cycles, i/o ports, graphics capabilities, **memory**, hard-disk, etc. ... may rely on accurate **collision detection**. ...

Master's thesis, University of Washington, Seattle, 1993 - [hitl.washington.edu](#)

[PS] Gigabit IP Network Devices

A Neogi - [View as HTML](#) - [Cited by 1](#) - [Web Search](#)

... routers, classification involves a multiple key **hash** lookup to ... cache and has minimum number of **memory** accesses. ... IP router on the path, to perform loop **detection**. ...

Research Proficiency Examination Report: Section, 1999 - [ecsl.cs.sunysb.edu](#)

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M Ginsberg, DS Nau, SJJ Smith, K Sycara, JF Allen, ... - [Web Search](#)

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The Knowledge Engineering Review, 1999 - [journals.cambridge.org](#)

[PDF] Efficient annotated terms

MGJ van den Brand, HA de Jong, P Klint, PA Olivier - [Cited by 108](#) - [Web Search](#)

... human-readable form depends on the actual blob **content**. ... pointer in each object for **hash** chaining, which in ... Other solutions for **collision** resolution will either ...

Software, Practice & Experience, 2000 - [doi.wiley.com](#) - [doc.telin.nl](#) - [doc.freeband.nl](#) - [adam.wins.uva.nl](#) - [all 16 versions »](#)

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... No **detection** of server failure. ... of the MD5 **hash** are used, the probability of **collision** is increased, roughly from 1 in 2 128 to 1 in 2 32

Massachusetts Institute of Technology, May, 2000 - pdos.lcs.mit.edu

[PDF] Information Technology Measurement and Testing Activities at NIST

MD Hogan, LJ Carnahan, RJ Carpenter, DW Flater, JE ... - [View as HTML](#) - [Web Search](#)

... ROM Compact Disk- Read Only **Memory** CEC ERIM ... Carrier Sense Multiple Access with **Collision**

Detection DARPA Defense ... Automatic Computer SHS Secure Hash Standard SI ...

Journal of Research of the National Institute of Standards ..., 2001 - nvl.nist.gov - nvl-p.nist.gov - mel.nist.gov

[PS] A Massively Parallel Architecture for Associative-Based Artificial Intelligence

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... 8.1 Relational **Database** Parallelization (after [FKS85]) : : : 184 8.2

Other Algorithms ... **Content**-addressable **memory** refers to a ...

University of California, Santa Cruz, 1995 - cse.ucsc.edu - cse.ucsc.edu

[PDF] A layered hyperanimation system

YC Hung... - [Web Search](#)

... as Figure 3 indicates, the animation **collision** occurs when ... The hyperlink event **detection**

module is the administrator that ... presented in this study is **hash** tables ...

Software - Practice and Experience, 2000 - doi.wiley.com - doi.wiley.com - portal.acm.org

[PS] A Natural Language Parser with Interleaved Spelling Correction Supporting Lexical Functional Grammar ...

MA Elmi - [View as HTML](#) - Cited by 8 - [Web Search](#)

... 174 9.2.1 Spelling Error **Detection**. ... Page. Table 6.6 **Collision**

Report for 1552 Words with **Hash** Size 1949.....138. ...

Illinois Institute of Technology, Chicago, IL, 1994 - cs.iit.edu - csam.iit.edu - portal.acm.org - portal.acm.org

[PDF] Cryptography and Evidence

M Roe - [View as HTML](#) - Cited by 18 - [Web Search](#)

... will be reducing m in length with a **collision**-free **hash** ... low-level" signature operation

on the padded **hash**, and then ... Bob to reveal to her the **content** of the ...

Doct. Dissert., Univ of Cambridge, UK, 1997 - isis.poly.edu - chiark.greenend.org.uk - cl.cam.ac.uk - opencik.org - [all 7 versions »](#)

[PS] Applications of Combinatorial Designs to Communications, Cryptography, and Networking

CJ Colbourn, JH Dinitz, DR Stinson - [View as HTML](#) - Cited by 14 - [Web Search](#)

... Other approaches have included frequency division, time division, **collision**

de- tection or some type of network synchronization. ...

London Math. Soc. Lecture Note Ser, 1999 - cacr.uwaterloo.ca - cacr.math.uwaterloo.ca - lottodesigner.150m.com -

cacr.math.uwaterloo.ca - [all 7 versions »](#)

[PS] Reliable Multicast for Publish/Subscribe Systems

Q Sun - [View as HTML](#) - [Web Search](#)

... Examples of **content**- ... In these situations, we first verify, using small **hash** ... gets the

message, and sending out heartbeats to facilitate loss message **detection**. ...

Massachusetts Institute of Technology, Dept. of Electrical ..., 2000 - mit.edu - mit.edu - lost-contact.mit.edu - graphics.csail.mit.edu

- [all 7 versions »](#)

[PDF] Algorithms for Routing Lookups and Packet Classification

P Gupta - [View as HTML](#) - Cited by 19 - [Web Search](#)

... PS (PE) indi- cates that this prefix-start (prefix-end) is also the **memory**-start

(**memory**-end) marker.. ... 120 Figure 4.8 The tuples and associated **hash** tables in ...

A dissertation submitted to the department of computer ..., 2000 - www-cs-students.stanford.edu - netlab.cs.tsinghua.edu.cn -

klamath.stanford.edu - cchen2.et.ntust.edu.tw - [all 7 versions »](#)

[PDF] Automated Synthesis and Optimization of Robot Configurations: An Evolutionary Approach

C Leger - [View as HTML](#) - Cited by 10 - [Web Search](#)

... a library of commonly required simulation capabilities such as ki- nematic and dynamic

simulation, actuator modeling, **collision** **detection**, and controllers for ...

Ph. D. Thesis, 1999 - darwin2k.com - frc.ri.cmu.edu - robotics.jpl.nasa.gov - neo.lcc.uma.es - [all 9 versions »](#)

Great Britain

J Home - [Cached](#) - [Web Search](#)

... CoCrPtB alloys with increased boron **content** and method ... imaging for enhancing optical

detection of edges ... Smith, Vehicle scheduling and **collision** avoidance system ...

IBM Journal of Research and Development, 2001 - research.ibm.com - research.ibm.com

[PDF] Efficient Algorithms for Sorting and Synchronization

A Tridgell - [View as HTML](#) - Cited by 42 - [Web Search](#)

... ing block signature information followed by a simple **hash** search algorithm

for block. ... costs and the **memory** requirements of the algorithm. ...

Doktorarbeit, Australian National University, 1999 - cri.ensmp.fr - samba.anu.edu.au - ccache.samba.org - comlab.ox.ac.uk - [all 9 versions »](#)

[PDF] A Secure Authentication Infrastructure for Mobile Communication Services over the Internet

ID la Tour, G Bochmann, JY Chouinard - [View as HTML](#) - [Web Search](#)

... to access anything in a second **database**, but be. ... Electronic solutions are based on

hash-algorithms, MAC ... **content** becomes disputable, nobody can deny that the ...

Communication and Multimedia Security Conference (CMS'2001, 2001 - beethoven.site.uottawa.ca

[PS] The Exokernel Operating System Architecture

DR Engler, MF Kaashoek - [View as HTML](#) - [Cited by 15](#) - [Web Search](#)

... As a result, in an exokernel system, untrusted software (eg, library operating systems)

can implement abstractions such as virtual **memory**, file systems, and ...

Massachusetts Institute of Technology, Oct, 1998 - amsterdam.lcs.mit.edu - pdos.lcs.mit.edu - cs.ttu.edu - portal.acm.org - [all 5 versions »](#)

[PDF] A Bibliography of Papers in Lecture Notes in Computer Science (1997), Part 1 of 2

NHF Beebe - [View as HTML](#) - [Web Search](#)

... **Collision**-Free [851, 831]. **Collision**-Resistant [2178]. Collisions [1025]. ... Contacts

[1839]. Containment [1245, 1834, 2, 3]. **Content** [2134, 1051, 2376, 1061]. ...

Way, 1837 - math.utah.edu - math.utah.edu - math.utah.edu

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